

ATTORNEY DOCKET NO.
TI-37052 (032350.B543)

1

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Prasun K. Raha
Serial No.: Unassigned
Filing Date: November 25, 2003
Title: *Generating An Oscillating Signal According
 To A Control Current*

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97, and 1.98, that the document listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), Applicant makes no representation that this document qualifies as prior art or that this document is material to patentability of the present application or that a search has been made.

ATTORNEY DOCKET NO.
TI-37052 (032350.B543)

2

PATENT APPLICATION

Since the present Application was filed after June 30, 2003, a copy of any U.S. Patent and any U.S. Patent Application Publication cited on the attached PTO Form 1449 is not being submitted with this Information Disclosure Statement pursuant to the July 11, 2003 waiver of 37 C.F.R. S 1.98(a)(2)(i) by the U.S. Patent and Trademark Office.

Respectfully submitted,

BAKER BOTTS L.L.P.
Attorneys for Applicant



Keiko Ichiye
Reg. No. 45,460

Correspondence Address

2001 Ross Avenue
Suite 600
Dallas, Texas 75201-2980
(214) 953-6494

Date: November 25, 2003

Customer Number:

23494

Patent Trademark Office

PTO-1449	Application No. Unknown		Applicant(s) Prasun K. Raha	
	Docket Number TI-37052 (032350.B543)	Group Art Unit Unknown	Filing Date November 25, 2003	

**Information Disclosure Citation
In an Application**

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						
	M						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	N							
	O							
	P							
	Q							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	R	Hiok-Tiaq Ng and David J. Allstot, CMOS Current Steering Logic for Low-Voltage Mixed-Signal Integrated Circuits, IEEE Transactions On Very Large Scale Integration (VLSI) Systems, Vol. 5, No. 3, pages 301-308.	September 1997
	S		
	T		

EXAMINER	DATE CONSIDERED
----------	-----------------

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:768347.1